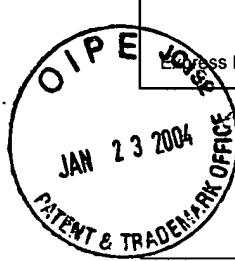


01-26-04



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Dated: _____

Docket No.: 20046/0200501-US0
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Ralf Brederlow

Application No.: 10/714,536

Confirmation No.:

Filed: November 13, 2003

Art Unit: N/A

For: POLYMER TRANSISTOR
ARRANGEMENT, INTEGRATED CIRCUIT
ARRANGEMENT AND METHOD FOR
PRODUCING A POLYMER TRANSISTOR
ARRANGEMENT

Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed within three months of the U.S. filing date (37 CFR 1.97(b)(1)).

A copy of each document on the PTO/SB/08 is attached. Pursuant to the Notice issued by the United States Patent and Trademark Office dated July 11, 2003 waiving the requirements of 37 C.F.R. 1.98(a)(2)(i), a copy of the United States Patent on PTO/SB/08a is not submitted.

A concise explanation of relevance of Citations CC and CD listed on form PTO/SB/08 is attached.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed documents.

The Commissioner is authorized to charge any deficiency of up to \$300.00 or credit any excess in this fee to Deposit Account No. 04-0100.

Dated: January 23, 2004

Respectfully submitted,

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Attorneys/Agents For Applicant

Concise Explanation

Citation No. CC - pages 1415-1423 describes the general structure and operation of temperature-compensated reference diodes.

Citation No. CD - pages 555-560 describes different means for generating a reference voltage. In particular, reference voltage sources including so called zener diodes are described.



PTO/SB/08a/b (08-03)

Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449A/B/PTO				Complete if Known	
				Application Number	10/714,536
				Filing Date	November 13, 2003
				First Named Inventor	Ralf Brederlow
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	20046/0200501-US0

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (<i>if known</i>)			
AA	US-6,455,873-B1		09-24-2002	Lonergan	

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³ -Number ⁴ -Kind Code ⁵ (<i>if known</i>)			
BA	WO-01/01452-A2		01-04-2001	Jackson, Thomas et al.	T ⁶

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
	CA	C.D. Dimitrakopoulos, et al.; "Organic thin-film transistors: A review of recent advances"; IBM J. Res. & Dev., vol. 45 no. 1, January 2001; pages 11-27			
	CB	P.V. Necliudov, et al.; "Modeling of organic thin film transistors of different designs"; Journal of Applied Physics, vol. 88, number 11, 1 December 2000, pages 6594-6597.			
	CC	D. Nuhrmann; "Das grosse Werkbuch Elektronik ist 20 Jahre alt", vol. 2, 1998, Franzis ' Verlag Poing, ISBN 3 7723-6547-7, pages 1415-1423			
	CD	U. Tietze, C. Schenck "Halbleiterschaltungstechnik", Springer-Verlag, Berlin, 1993, pages 555-560.			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature	Date Considered
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Application No. (if known): 10/714,536

Attorney Docket No.: 20046/0200501-US0

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PTO/SB/08a with five documents
Concise explanation
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